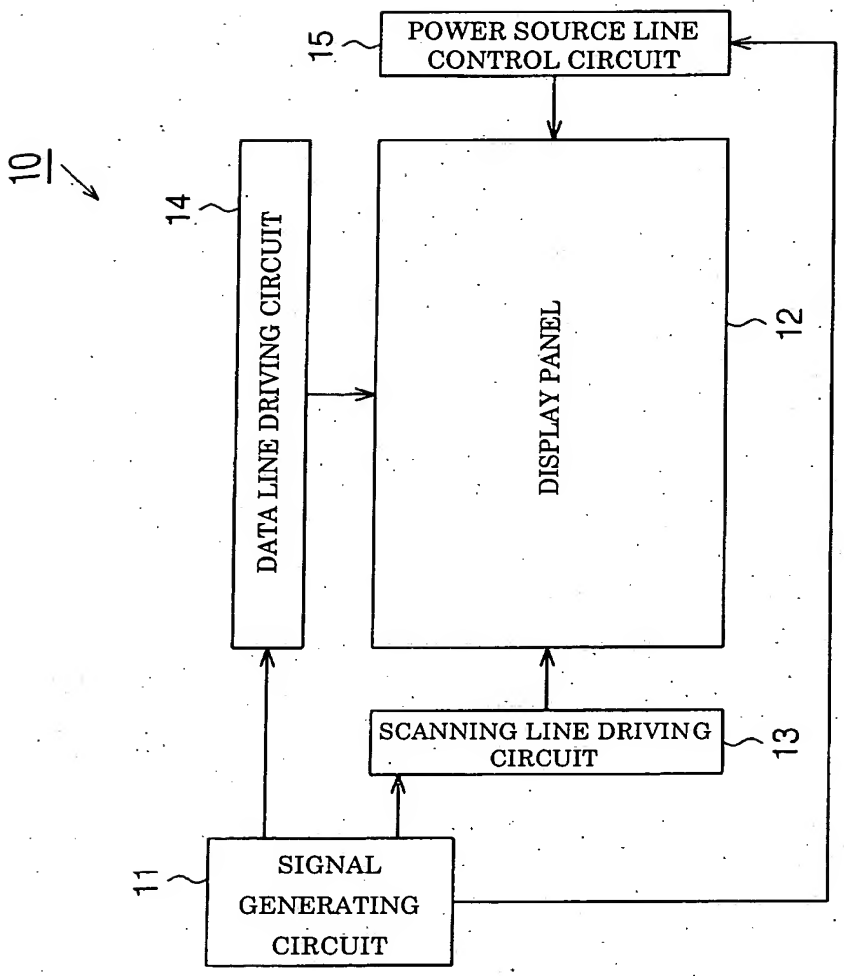
10
15
POWER SOURCE LINE CONTROL CIRCUIT
14
DATA LINE DRIVING CIRCUIT
DISPLAY PANEL
12
SCANNING LINE DRIVING CIRCUIT
13
11
SIGNAL GENERATING CIRCUIT

Fig. 1

Fig. 2

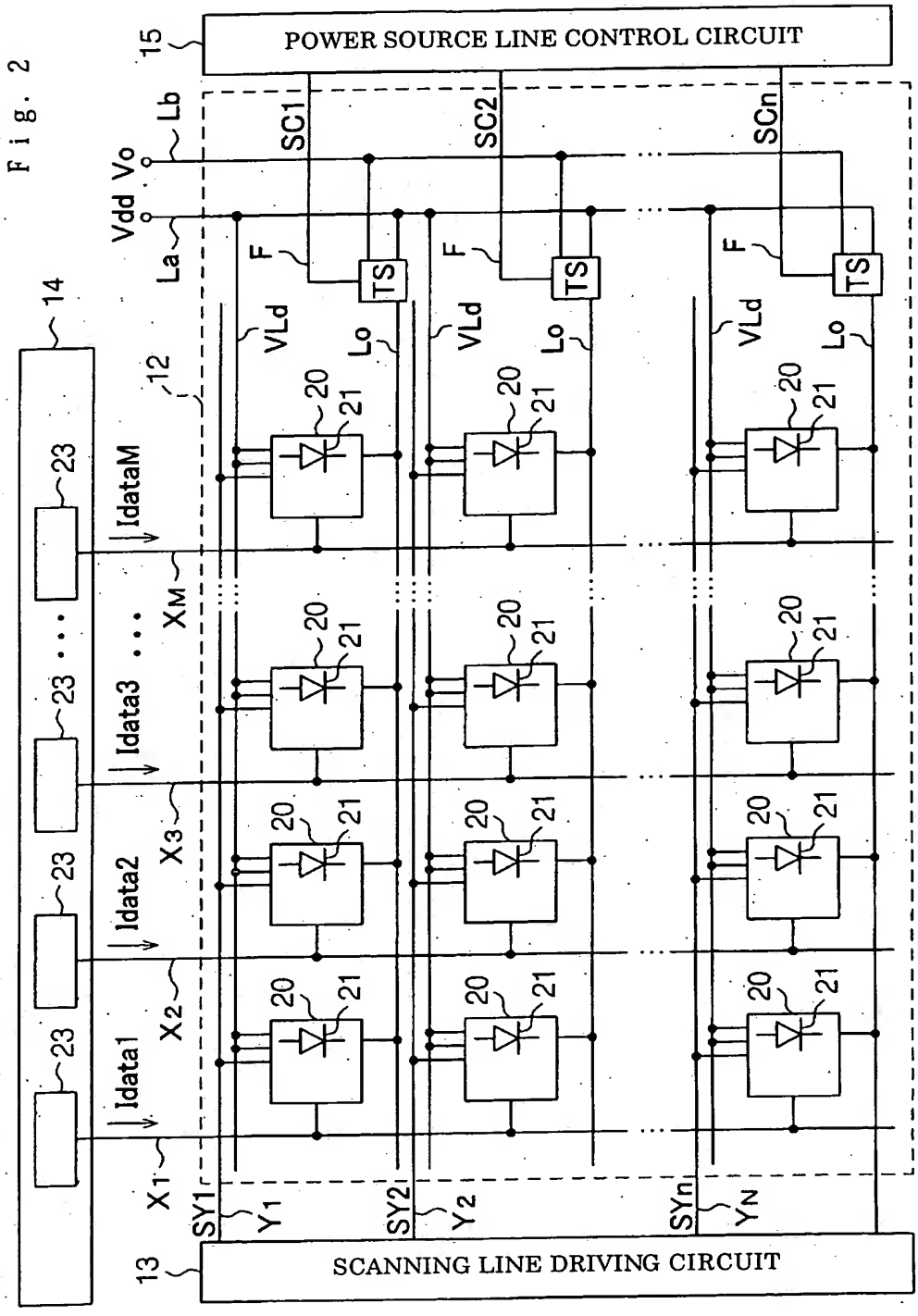
POWER SOURCE LINE CONTROL CIRCUIT
15
Lb
Vdd Vo
La
SC1
SC2
SCn
F
TS
VLd
Lo
12
14
20
21
23
IdataM
Idata3
Idata2
Idata1
XM
X3
X2
X1
SY1
SY2
SYn
Y1
Y2
YN
13
SCANNING LINE DRIVING CIRCUIT

Fig. 3

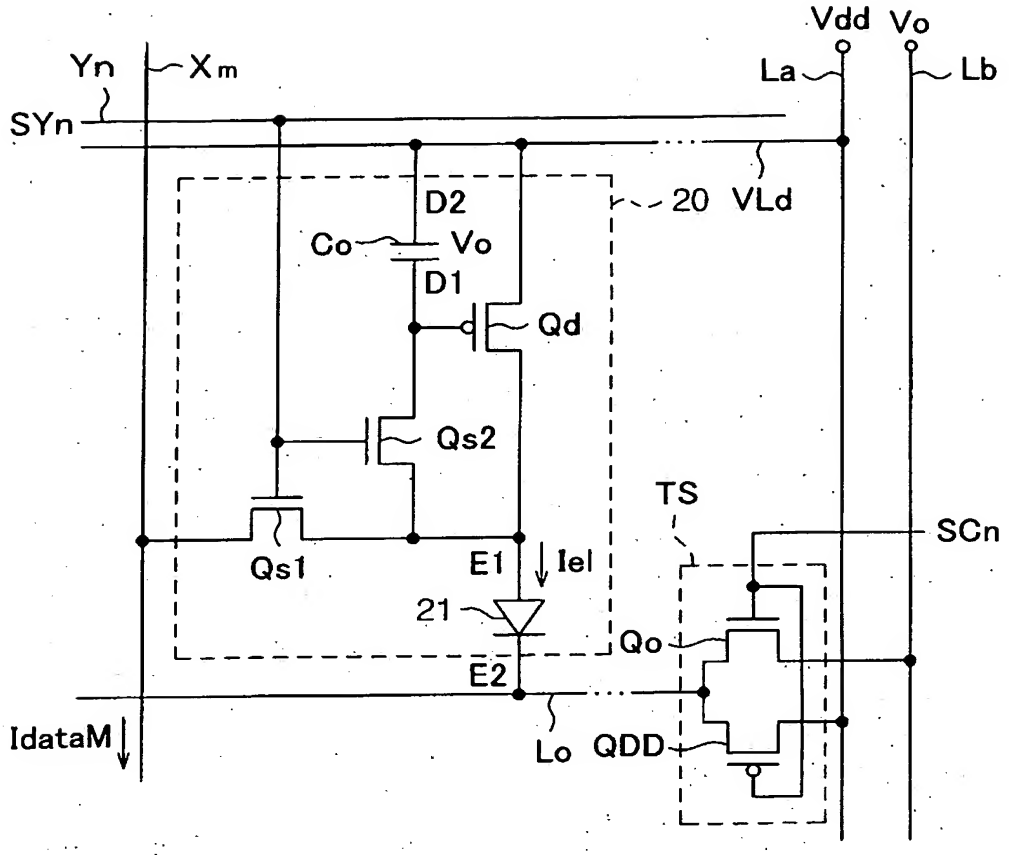
Vdd
Vo
Yn
Xm
La
Lb
SYn
20
VLd
D2
Co
Vo
D1
Qd
Qs2
TS
SCn
Qs1
E1
Iel
21
Qo
E2
IdataM
Lo
QDD

Fig. 4

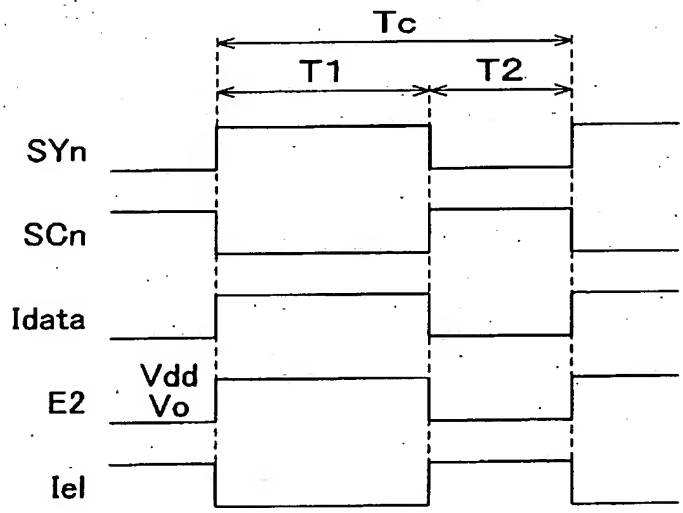
Tc
T1
T2
SYn
SCn
Idata
Vdd
Vo
E2
Iel

Fig. 5

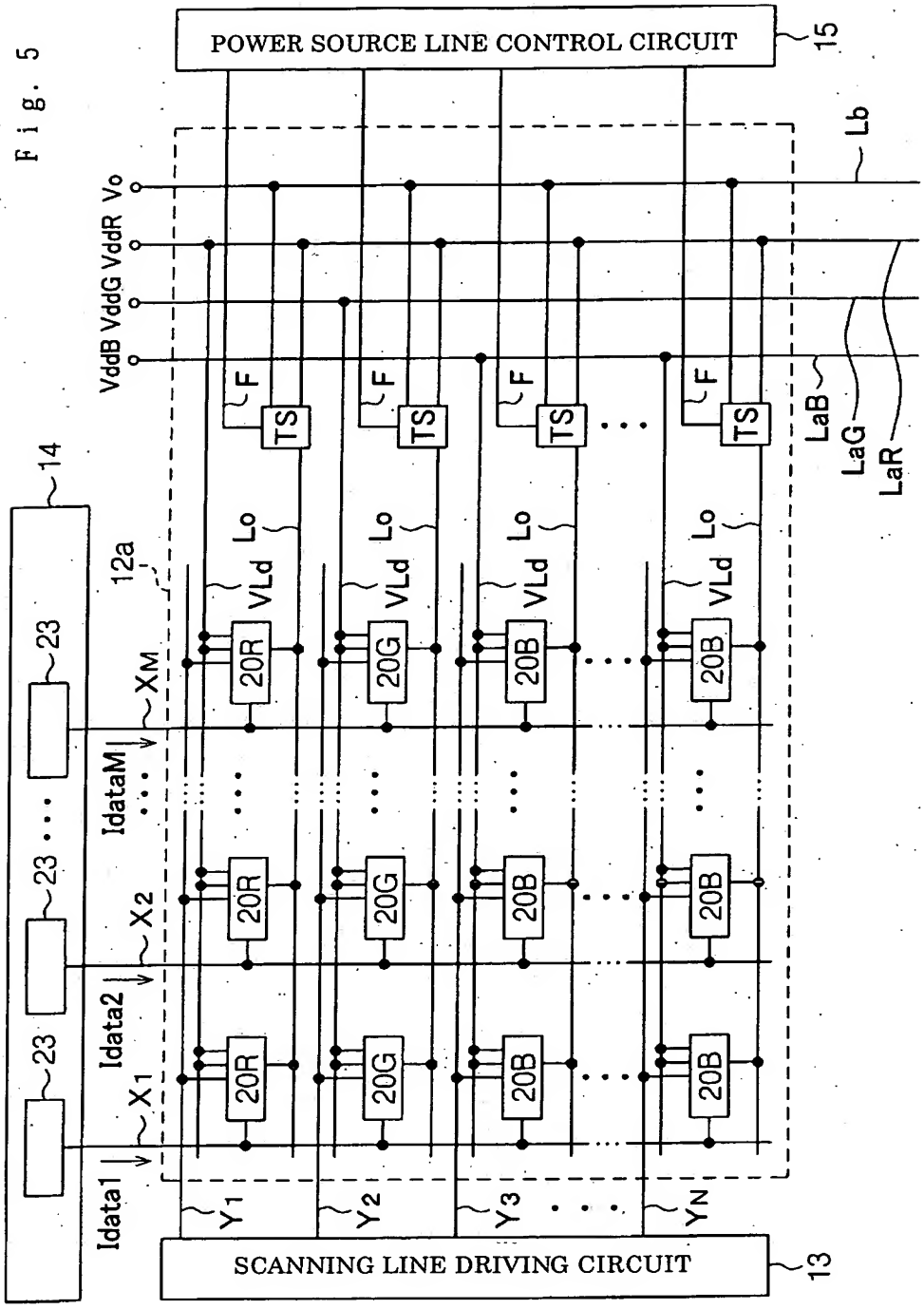
POWER SOURCE LINE CONTROL CIRCUIT
15
Lb
Vo
VddR
VddG
VddB
LaB
LaG
LaR
F
TS
Lo
VLd
12a
14
23
XM
IdataM
X2
Idata2
X1
Idata1
20R
20G
20B
Y1
Y2
Y3
YN
SCANNING LINE DRIVING CIRCUIT
13

Fig. 6